

**INTEGRATED CIRCUIT DELIVERING LOGIC LEVELS AT
A VOLTAGE INDEPENDENT FROM THE MAINS VOLTAGE,
WITH NO ATTACHED REGULATOR FOR THE POWER
SECTION, AND CORRESPONDING COMMUNICATION**

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MODULE

ABSTRACT OF THE DISCLOSURE

The invention concerns an integrated circuit comprising means of delivering, on at least one output, a predetermined output voltage representative of a logic level, means of distributing a mains voltage and means of generating an internal voltage reference lower than the mains voltage, comprising means of connecting the mains voltage to the output and means of limiting and/or detecting the voltage at the output at the predetermined output voltage value, taking into account the reference voltage.

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